

FIG. 1

201

demodulator 102 writes the demodulated ECC block 107 into the data buffer 106.

202

the ECC decoder 114 reads the ECC block 107 of the PI direction to perform the error correction decoding, then writes the corrected part of the ECC block 107 into the data buffer 106.

203

the ECC decoder 114 reads the ECC block 107 of the PO direction to perform the error correction decoding, then writes the corrected part of the ECC block 107 into the data buffer 106.

204

the descrambler and EDC check 116 reads the corrected main data 108 stored in data buffer 106 for descrambling the main data 108 and checking whether errors in the main data 108 are corrected.

205

ATAPI ( Advanced Technology Attachment Package Interface ) 118 reads the main data 108 stored in the data buffer 106, then descrambles and transmits the main data to the host.

FIG . 2

09828202-04001  
T06040-20282860

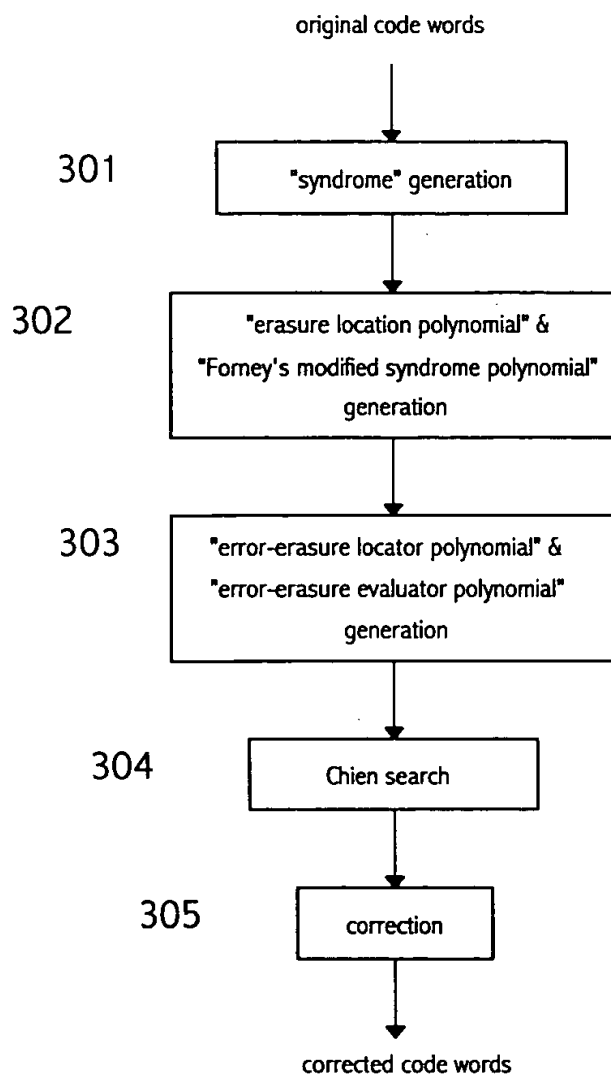


FIG . 3

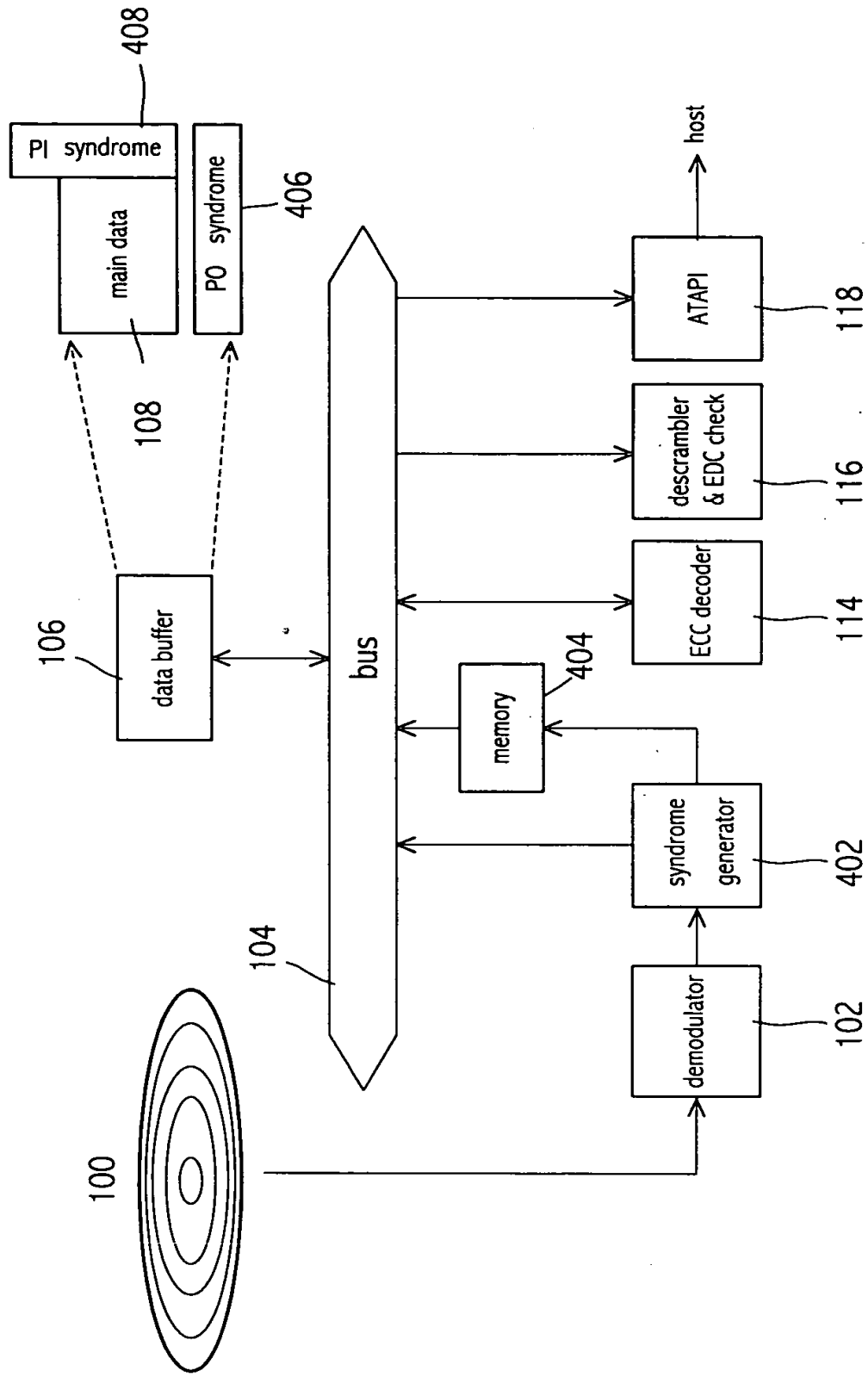


FIG. 4

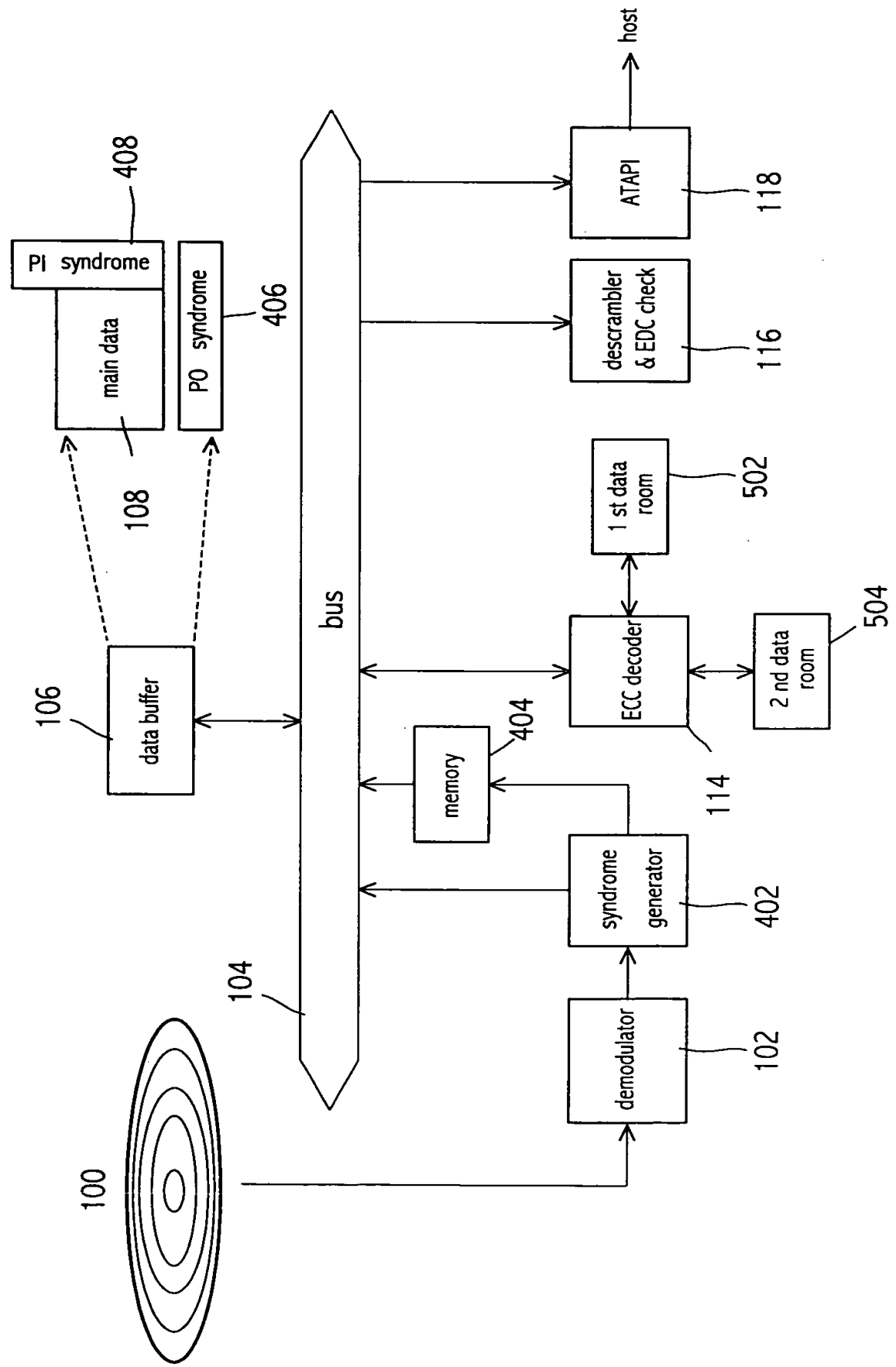


FIG. 5

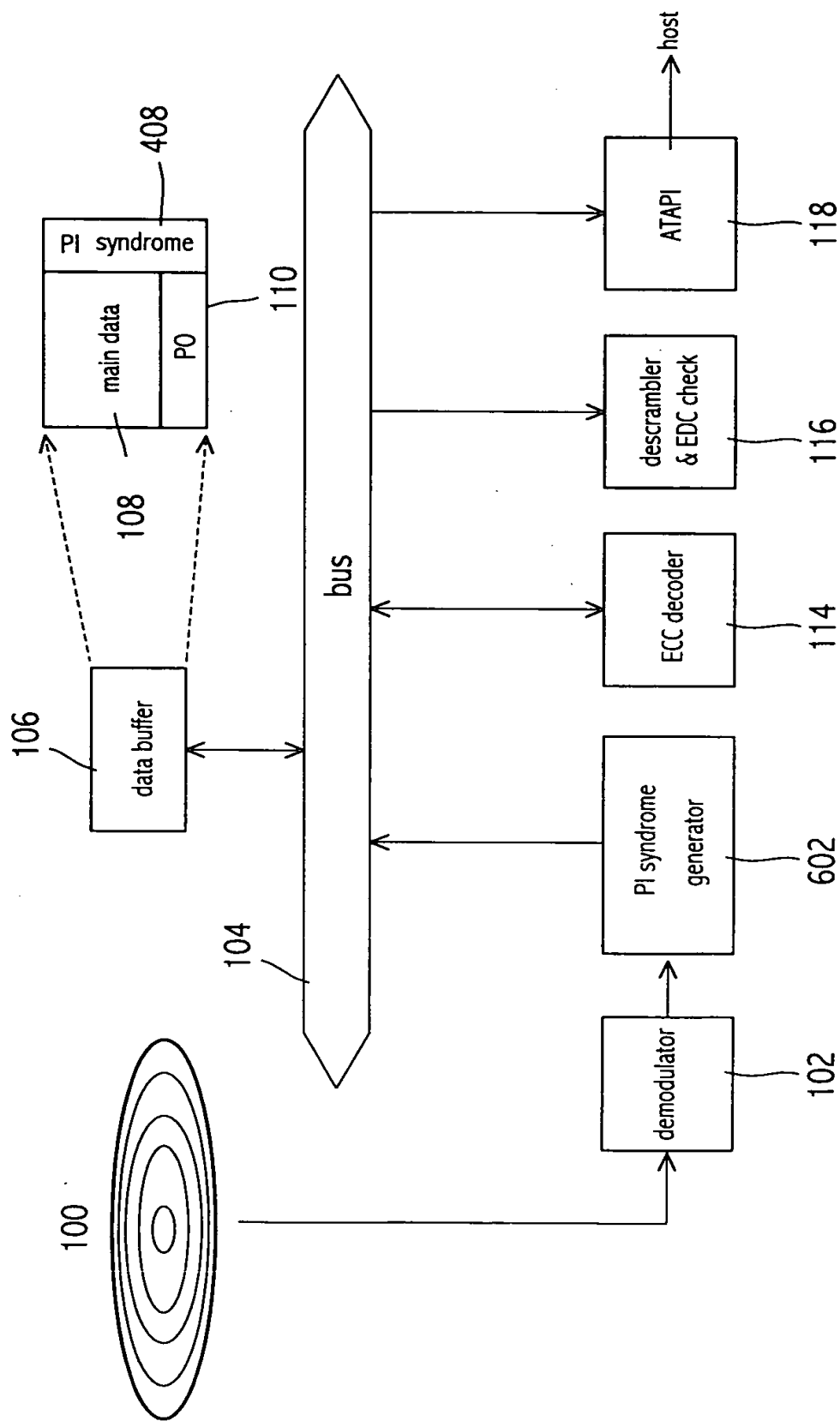


FIG. 6

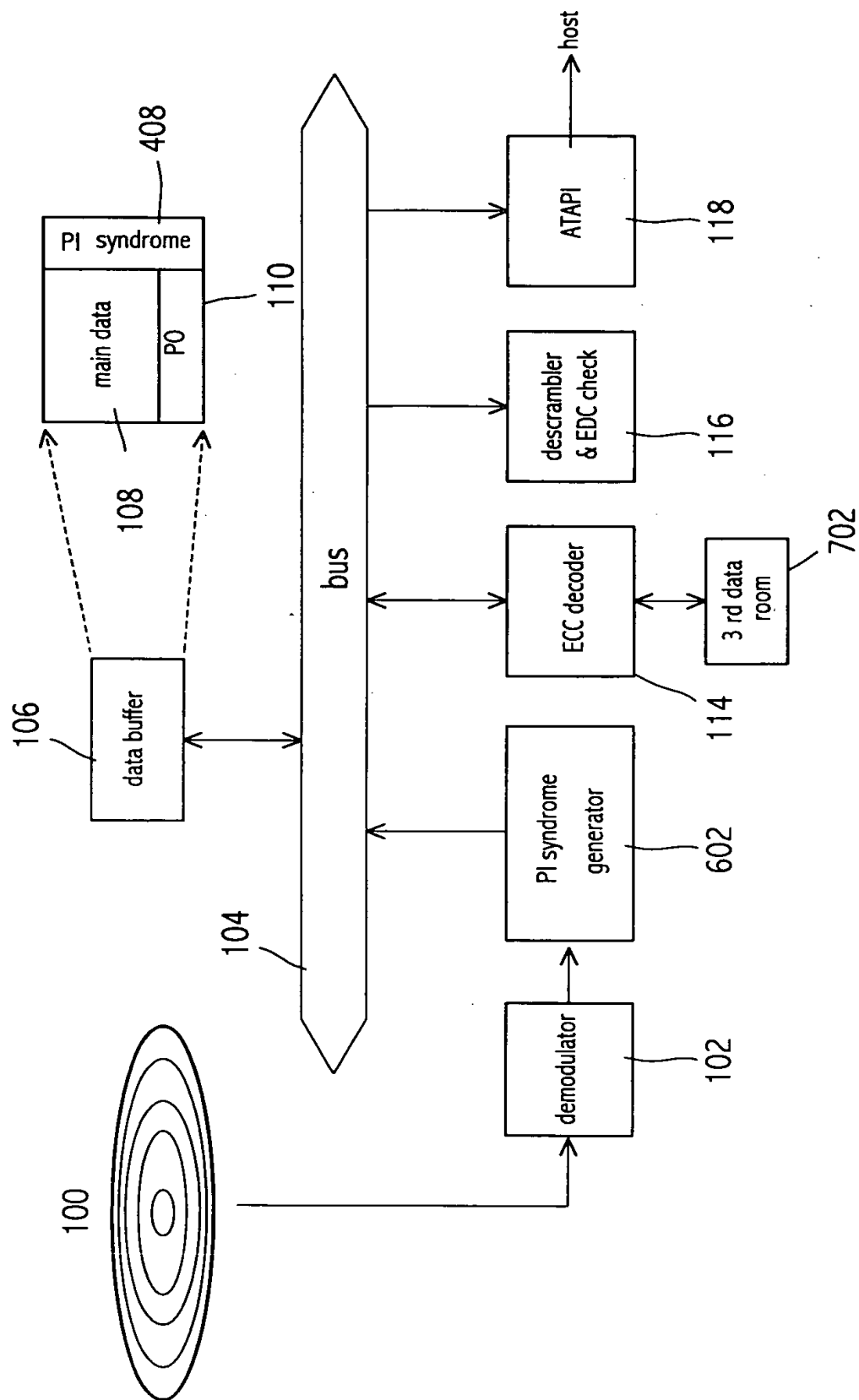


FIG. 7

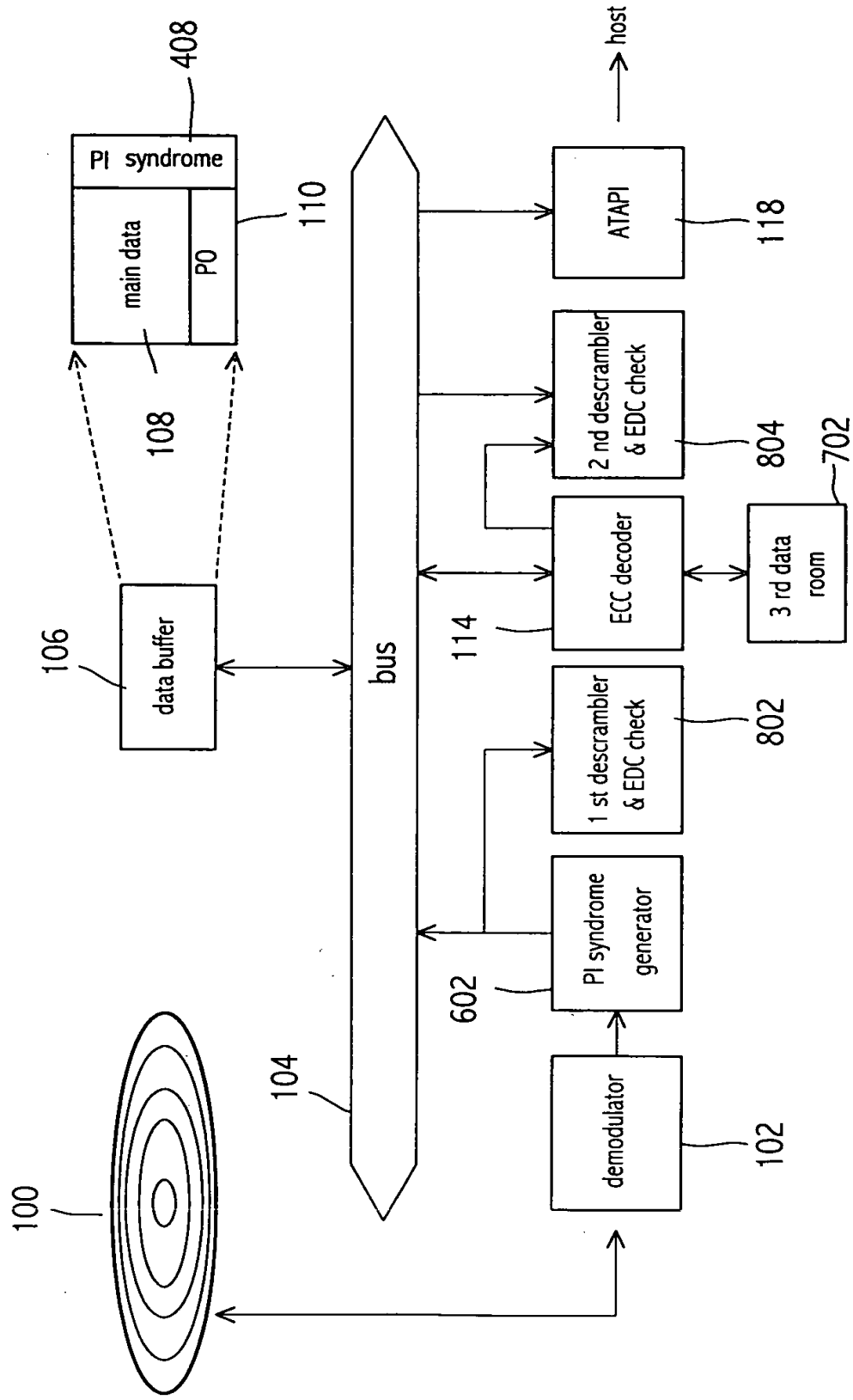


FIG. 8